



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Randy Skocypec et al.

Serial No.: 10/622,307

Filed: July 18, 2003

For: Cleaning Semiconductor  
Wafers

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Art Unit: 1746

Examiner: Bibi Sharidan Carrillo

Docket: ITL.1017US  
P16704

Assignee: Intel Corporation

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Deposit: February 7, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
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